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Macintosh SE/30 Logic Board Upgrade: Specs (Discontinued)

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Processor

- MC68030 32-bit internal Harvard architecture
- 15.667 MHz clock frequency
- Built-in Paged Memory Management Unit (PMMU)
- 256-byte instruction and data caches

Coprocessor

- MC68882 floating-point unit (follows IEEE standards)

Interfaces

- Two Apple Desktop Bus connectors for communication with keyboard, mouse, and other input devices
- 030 Direct Slot, supporting full 32-bit address and data lines through 120-pin EuroDIN connector
- Two RS-232/RS-422 serial ports, 230.4K baud maximum; use mh-8 connectors (up to 0.920 Mbit/sec, if clocked externally)
- External disk drive interface
- SCSI interface
- Stereo sound port for external audio amplifier

Sound Generator

- Apple Sound Chip (ASC) including four-voice, wave-table synthesis and stereo sampling generator, capable of driving stereo mini-phone-jack headphones or other stereo equipment
- Mixed stereo monophonic sound output through internal speaker

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