Tech Info Library

Performa 600: Control Chips

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Security:       Everyone


Performa 600: Control Chips


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TOPIC -----------------------------------------------------------

This article describes the Macintosh Performa 600's control chips.

DISCUSSION ------------------------------------------------------

VASP
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The VASP controls all timing, video generation, memory mapping, sound, and
clock generation. VASP is a modification of the V8 ASIC used in the
Macintosh LC and Macintosh LC II.

Ariel
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Ariel is the 256x24 bit color look-up table (CLUT) for Performa 600.

CoMBo Chip (85C80)
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The CoMBo chip provides identical serial and SCSI interface control for
Performa 600.  Performa 600 provides on-board SCSI termination when no
external SCSI device is connected to the DB-25 connector.

Egret (68HC05)
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The Egret chip provides keyboard scanning, real-time clock, ADB interface,
parameter RAM storage, power on reset, and power supply control.


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