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Power Macintosh: Which Models Support Memory Interleaving?(6/96)

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TOPIC -----

Do any of the Power Macintosh computers or Workgroup Servers use memory interleaving? I heard that the PowerPC 601 chip will not support interleaving.

DISCUSSION -----

Memory interleaving is a function of the memory controller not the microprocessor. Therefore, some Power Macintosh models based on the same microprocessor will support memory interleaving while others based on the same microprocessor will not.

For example, the Power Macintosh 7500, which is based on the PowerPC 601 microprocessor, supports memory interleaving. The Power Macintosh 7200, which is also based on the PowerPC 601 processor, does not support memory interleaving. The difference is that the Power Macintosh 7500 uses the HammerHead ASIC memory controller which supports memory interleaving and the 7200 uses the Platinum IC which does not support memory interleaving. The Power Macintosh 6100, 7100, and 8100 computers use the High-speed Memory Controller (HMC) which also does not support memory interleaving.

Begin_Table

POWER MACINTOSH SERIES NUMBER	MICROPROCESSOR	MEMORY CONTROLLER	MEMORY INTERLEAVING
Power Macintosh 6100	601	HMC	NO
Power Macintosh 6150	601	HMC	NO
Power Macintosh 7100	601	HMC	NO
Power Macintosh 7200	601	Platinum IC	NO
Power Macintosh 7250	601	Platinum IC	NO
Power Macintosh 7500	601*	HammerHead ASIC	YES
Power Macintosh 7600	604*	HammerHead ASIC	YES

Power Macintosh 8100	601	HMC	NO
Power Macintosh 8150	601	HMC	NO
Power Macintosh 8500	604*	HammerHead ASIC	YES
Power Macintosh 8550	604*	HammerHead ASIC	YES
Power Macintosh 9150	601	HMC	NO
Power Macintosh 9500	604*	HammerHead ASIC	YES

* Removeable/upgradeable microprocessor card

End_Table

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26 Jun 1996 - Updated with information about more models.

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